Product Education at a glance



# Atrial and Ventricular Rates Below the Programmed Lower Rate Limit

On occasion, clinicians reviewing device diagnostics such as electrograms or rate histograms may observe paced or sensed atrial or ventricular rates below the programmed **Lower Rate Limit** (**LRL**). Frequently, these temporary "slow" rates are normal device function resulting from programmed features or standard sensing algorithms.

Atrial and/or ventricular rates below the **LRL** can be the result of oversensing, but can also be attributed to one of the following:

- Rate Hysteresis
- A-V conduction during paced A-V function
- Premature Ventricular Contractions (PVCs)

## **Rate Hysteresis**

Rate Hysteresis is a rate enhancement feature designed to promote longer periods of intrinsic rhythm by temporarily allowing both atrial and ventricular intrinsic and paced rates to fall below the programmed LRL (or sensor-indicated rate). If the Rate Hysteresis feature is programmed On, the programmed LRL (or sensor-indicated rate) is lowered by the programmed Hysteresis Offset (ranging from 5-80 ppm). This allows a patient's atrial and ventricular rate to fall below the LRL for one or more cardiac cycles when atrial or ventricular intrinsic activity is detected within the programmed Hysteresis Offset window.

If the low pacing rates are the result of **Rate Hysteresis** and these low rates are undesirable for a given patient, clinicians may consider reducing the **Hysteresis Offset** to a lower value (to increase the rate) or programming the **Rate Hysteresis** feature **Off**.

#### **A-V Conduction**

**A-V conduction can delay ventricular pacing**. If a patient is paced at or near the **LRL** in both the atrium and the ventricle, and an atrial paced event is intrinsically conducted to the ventricle, it may cause the next V-V interval to be extended beyond the programmed **LRL**.

### **SUMMARY**

Review of rate histograms or electrocardiograms may occasionally reveal atrial or ventricular events below the programmed Lower Rate Limit (LRL). Although these lowerthan-expected rates are typically the result of normal device function, clinicians may be interested in evaluating the root cause of the rates. The timing associated with Rate Hysteresis, intrinsic AV conduction, or premature ventricular contractions (PVCs) should be considered.

This article provides tips to help recognize and interpret events observed below the LRL.

ICD: Implantable Cardioverter Defibrillator

CRT-D: Cardiac Resynchronization
Therapy Defibrillator

CRT-P: Cardiac Resynchronization
Therapy Pacemaker

CRM PRODUCTS REFERENCED All ICDs, CRT-Ds, CRT-Ps and Pacing Systems

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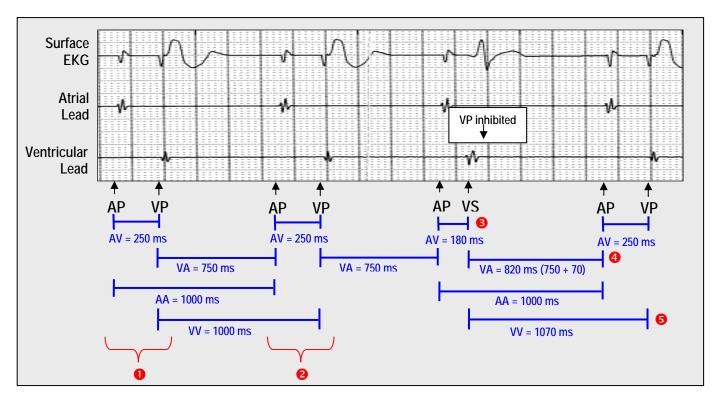


Figure 1. A-V conduction can delay ventricular pacing.

# **Premature Ventricular Contraction**

A PVC can reset cycle timing and delay atrial pacing. Figure 2 depicts a programmed LRL of 60 ppm (1000 ms) and an AV Delay of 250 ms. Following the second contraction cycle a PVC occurs approximately 680 ms after the previous ventricular paced event ①. Adding the programmed VA interval of 750 ms (LRL minus AV Delay) ② to the PVC places the next paced atrial event a total of 1680 ms from the previous atrial event (250 + 680 + 750) ③, or 36 ppm, which is significantly slower than the LRL of 60 ppm.

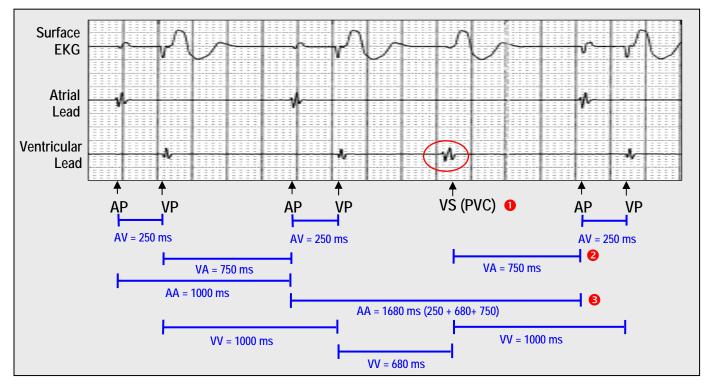


Figure 2. A PVC can delay atrial pacing.